CURRICULUM VITAE

Dr. SANDEEP K. ARYA

Professor
Department of ECE
G.J. University of Science & Technology
Hisar (Haryana)
PIN-125001

D.O.B: 5th May, 1967

Phone: 094163-97567 (Mobile) <u>E-mail</u>: arya1sandeep@gmail.com

EDUCATIONAL QUALIFICATION:

Examinatio	Year	Division	Marks	Percentage	Name of Board/
n				Marks	University
High School	1982	First	333/500	66.6	UP Board, Allahabad
Intermediate	1984	First	327/500	65.4	UP Board, Allahabad
B. Sc.	1986	First	637/950	67.0	Meerut University
B. Tech.	1991	First with	3569/	71.4	REC Kururkshetra
		Honours	5000		
M. Tech.	1993	First with	1472/	73.6	REC Kururkshetra
		Distinction	2000		
Ph.D.	2008	NIT Kurukshetra			Kururkshetra University

TOPIC of Ph.D.:

"Higher-order Dispersion and Non-linearities in Optical Communication Systems and Networks".

EXPERIENCE:

- 1. **09.09.2010** to till date working as Professor and **09.09.2004** to **08.09.2010** worked as Reader/Associate Professor in Department of Electronics and Communication Engineering, Guru Jambheshwar University, Hisar and subsequently worked as Chairman of the department since 18.10.2004 to 17.10.2007 and 07.11.2008 to 17.10.2015.
- 2. 05.08.1997 to 05.08.2002 worked as Lecturer and from 05.08.2002 to 08.09.2004 worked as Lecturer (Sr. Scale), in the Department of Electronics and Communication Engineering, National Institute of Technology, Jalandhar.
- 3. 11.09.1996 to 04.08.1997 worked as Lecturer, in the Department of Electronics and Communication Engineering, SJP Institute of Engineering and Technology, Radaur, Yamuna Nagar.
- 4. 12.02.1994 to 10.09.1996, worked as Lecturer, in the Department of Electronics and Communication Engineering, SJP Polytechnic, Damla, Yamuna Nagar.
- 5. 16.09.1993 to 13.12.1993 worked as Lecturer (on adhoc basic), in the Department of Electronics and Communication Engineering, SJP Polytechnic, Damla, Yamuna Nagar.

COURSES TAUGHT:

At U.G. Level:

Reliability Engineering, Power Electronics, Industrial Electronics, Bio- Medical Electronics, Digital Electronics, Fiber and Opto- Electronics, Electromagnetic Theory, Antenna and Wave Propagation.

At P.G. Level:

IC Fabrication Technology, Digital Electronics and Advance optical communications.

At Ph.D. Level:

Communication Technologies.

COURSES DEVELOPED AT NIT JALANDHAR:

Reliability Engineering, Power Electronics and Industrial Electronics.

PUBLICATIONS:

More than 100 publications in SCI/ Scopus refereed journals, List is available at **Google scholars** https://scholar.google.com/citations?user=-r3vzEsAAAAJ&hl=en. With more than 493 citations and h index 12 and i-10 index 22.

COURSES DEVELOPED AT GJUS&T HISAR:

B.Tech. ECE credit based system implemented and courses were designed and M.Tech. VLSI Design and Embedded System started and courses were designed.

Course work for Ph.D. on Communication Technologies has been designed and implemented.

Labs Incharge: Advance communication lab

WORKSHPS/ SUMMER /WINTER SCHOOLS ATTENDED:

- 1. **Two week Short Term Course on "Power Electronics"**, TTTI, Chandigarh, May 9th 20th, 1994.
- 2. **Two week Pre-Service Training for Technical Teachers**, at Kurukshetra University, Kurukshetra, from June 5th –25th, 1995.
- 3. **Two week Short Term Course on "Educational Computing"**, Computer Science Department, TTTI, Chandigarh, December 18th –30th, 1995.
- 4. **One week Short Term Course on "Total Quality Management"**, sponsored by Indian Society for Technical Education, R.E.C. Jalandhar, Feb., 24th –28th, 1999.
- 5. Two weeks Winter School on "Micro-Controllers & Their Industrial Applications", sponsored by Indian Society for Technical Education, at GNEC Ludhiana, Dec., 20th 31st, 1999.
- 6. One week Short Term Course on "Research and Development", sponsored by Indian Society for Technical Education, R.E.C.Jalandhar, March 1st –5th, 2 000.
- 7. **One week Short Term Course on "MATLAB",** at NITTR Chandigarh, Jan. 19th 23rd, 2004.

- 8. Attended All India seminar on "Challenges in Quality Technical Education" conducted by Institution of Engineers, New Delhi, during 11-12th July, 2008.
- 9. Attended One day seminar on **"Faculty Development Program"** organized by HCL Infosystems Limited NOIDA, on 05th Feb. 2010.
- 10. **One week on "Incident and Emergency Management"**, sponsored by Ministry of Science and Technology, DST at Center for Disaster Management, Lal Bahadur Shastri National Academy of Administration, Mussorie, Dehradun, during 31st May to 04th June 2010.
- 11. Two week programme at Photonic IC's JEPPIX wef 28th oct 2014 to 10th 2014 at TU/E Eindhoven, Netherlands.

LIST OF SPONSORED PROJECTS COMPLETED:

- 1. Up-gradation of Power Electronics and Industrial Control Laboratory, sponsored by MHRD, Govt. Of India. (Completed in Dec. 2003, **Amount sanctioned: 5.00 Lacs**) at NIT Jalandhar, (**Principal investigator**).
- 2. Modernization of Industrial Electronics Lab, under MODROBS Scheme by AICTE New Delhi, completed at NIT Jalandhar (**Co-investigator**).

ADMINISTRATIVE DUTIES/ RESPONSIBILITIES:

AT GURU JAMBHESHWAR UNIVERSITY, HISAR:

- 1. Director IQAC, since July 2017 to May 2018.
- 2. **Chairman** of the Electronics and Communication Engineering Department since 18.10.2005 to 17.10.2007 and w.e.f. 07.11.2008 to 17.10.2015...
- 3. **Member** PGBOS of Electronics and Communication Engineering Department since 18.10.2007 to 06.11.2008.
- 4. **Member** UGBOS of Electronics and Communication Engineering Department since 18.10.2007 to 06.11.2008.
- 5. **Ex-officio Chairman** of the PGBOS of Electronics and Communication Engineering Department since 18.10.2005 to 17.10.2007 and w.e.f. 07.11.2008.
- 6. **Ex-officio Chairman** of the UGBOS of Electronics and Communication Engineering Department since 18.10.2005 to 17.10.2007 and w.e.f. 07.11.2008.
- 7. Member of University Standing Purchase committee-1during year 2010.

Place:	
Date:	Signature